REMARKS

Status of the Application

Claims 1, 11 and 15 are amended to recite the limitations of claim 26 and claim 26 is canceled. Hence, claims 1-25 and 27 are all the claims pending in the application. Claims 1-3, 11-12, 15-17 and also new claims 25 and 27 are rejected under 35 U.S.C. § 102(e) as allegedly anticipated by Morita (US Patent 6,750,840). Claims 5-6, 13 and 19-20 are rejected under 35 U.S.C. § 103(a) as allegedly being unpatentable over Morita. Claims 4, 7-10, 14, 18, and 21-24 are allowable if rewritten in independent form.

Claim Amendments

The features of claim 26 have been added to independent claims 1, 11 and 15, and claim 26 has been cancelled. Applicant therefore respectfully asserts that the claim amendments do not present matter requiring a new search of the art, and therefore respectfully requests the examiner to enter the Amendments.

Claim Rejections

The current Office Action maintains the same grounds of rejection over Morita (U.S. Patent 6,750,840) presented in the previous Office Action, and have extended the same rejection to the newly added claims. Applicant respectfully traverses these rejections.

Claim 1 recites "supplying said write-in voltages ... for a period corresponding to a distance from the selected row line to said end points; wherein said period corresponding to a

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distance increases as a function of the distance from the selected row line to said end points."

The grounds of rejection assert that this feature is taught in column 12, lines 51-59 of Morita.

This portion of Morita teaches that "based on the distance between the selected pixel and the

data-line drive circuit, a boosted, high voltage is supplied to the data line X in a given period

within the selection period." That is, Morita teaches boosting the *voltage* based on the distance

between the selected pixel and the data-line drive circuit. This is shown in corresponding figures

10 and 11, where voltage V1 is applied to pixel 1, voltage V2>V1 is applied to pixel 200 (pixel

200 being at a greater distance than pixel 1), and voltage V3>V2>V1 is applied to pixel 400.

Morita discloses varying the period in which these voltages are applied; however Morita does not

teach that the period "increases as a function of the distance from the selected row line to said

end points," as recited in claim 1. Rather, Morita discloses decreasing the period that the voltage

is applied in order to change the time required for the voltage in the pixel to reach a stable value.

As disclosed in column 12, lines 18-25,

> In FIG. 11B, the data signal voltage Vd is boosted in a period from
> t.1 to tb3. The period from t1 to tb3 is set shorter than the
> corresponding period from t1 to tb1 in FIG. 10B. Accordingly, the
> voltage reaches the predetermined voltage V1 at time tb4. In FIG.
> 11C, likewise, a period from t1 to tc3 is set shorter than the
> corresponding period from t1 to tc1 in FIG. 10C. Accordingly, the
> voltage reaches the predetermined voltage V1 at time tc4.

Thus, the time required for the voltage to reach the predetermined value is changed by changing

the period in which the data signal voltage is boosted (compare figures 10B and 10C to figures

11B and 11C, respectively. Note that the overshoot apparent in 10B and 10C is avoided in 11B and 11C). However, Morita does not teach or suggest that the period increases as a function of the distance from the selected row line to said end points.

Applicant therefore respectfully submits that claim 1 is not anticipated by Morita at least due to this difference, as well as additional recited features. Because independent claims 11 and 15 also recite this feature, they are not anticipated by Morita for the same reason. The remaining claims are not anticipated by Morita at least due to their dependencies, as well as additional recited features.

Conclusion

In view of the above, reconsideration and allowance of this application are now believed to be in order, and such actions are hereby solicited. If any points remain in issue which the Examiner feels may be best resolved through a personal or telephone interview, the Examiner is kindly requested to contact the undersigned at the telephone number listed below.

The USPTO is directed and authorized to charge all required fees, except for the Issue

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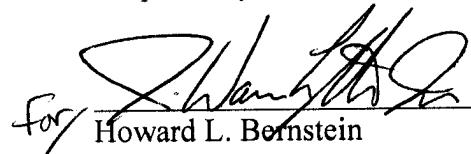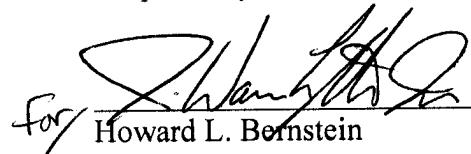
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